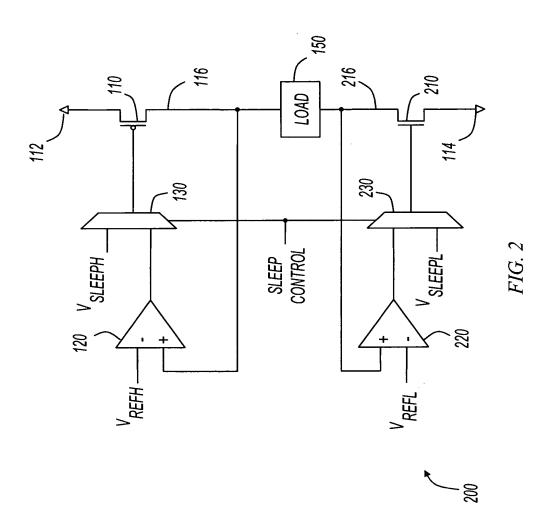
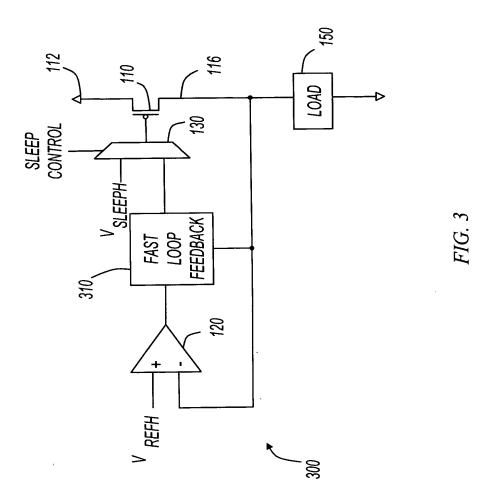
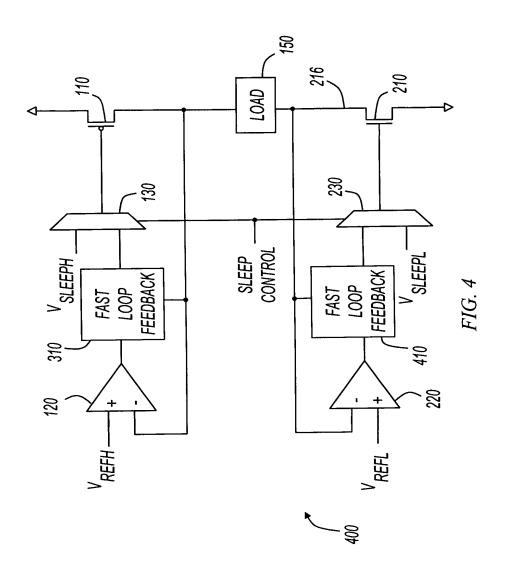


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## TITLE: REGULATED SLEEP TRANSISTOR APPARATUS, METHOD, AND SYSTEM INVENTORS NAME: Peter Hazucha et al. SERIAL No.: 10/673,822

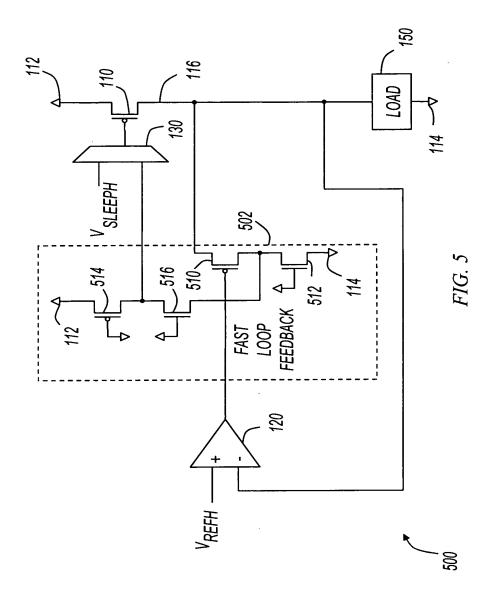


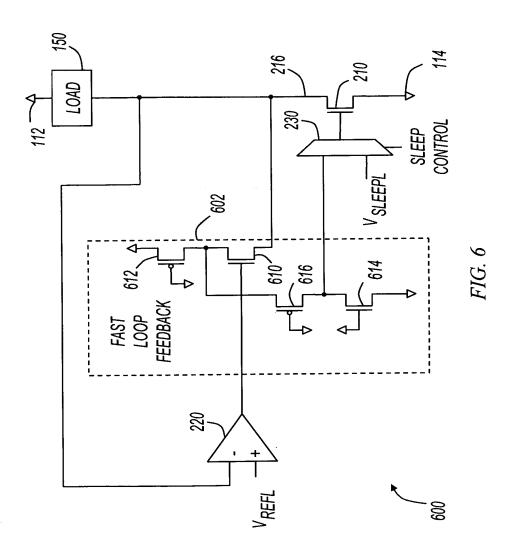


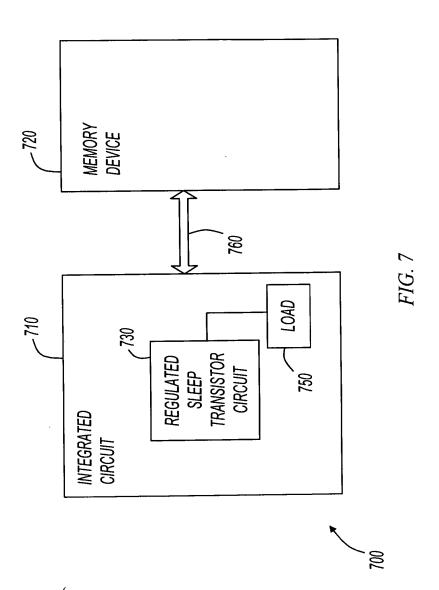


TITLE: REGULATED SLEEP TRANSISTOR APPARATUS, METHOD, AND SYSTEM

INVENTORS NAME: Peter Hazucha et al. SERIAL No.: 10/673,822







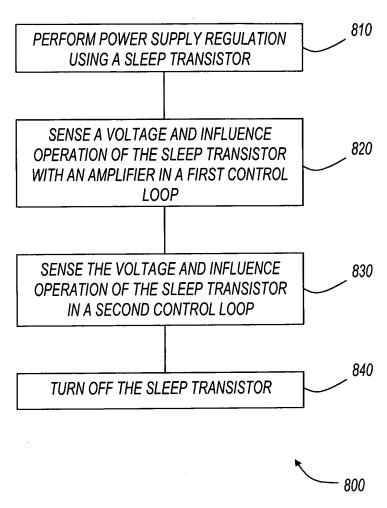


FIG. 8

## TITLE: REGULATED SLEEP TRANSISTOR APPARATUS, METHOD, AND SYSTEM

INVENTORS NAME: Peter Hazucha et al. SERIAL No.: 10/673,822

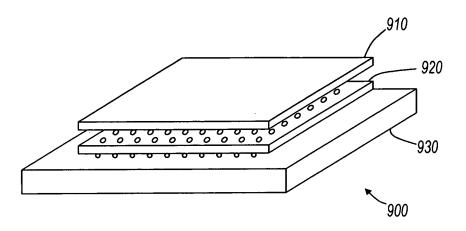


FIG. 9

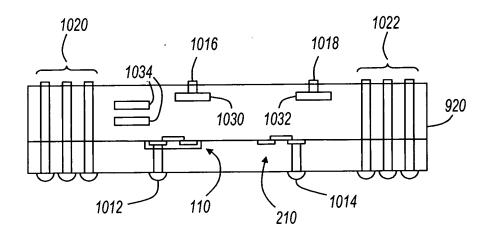


FIG. 10